

1-10-05

1005

Attorney Docket No.: 254-094-CIP4/C1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: Memory Expansion and Chip Scale Stacking System and Method  
Application No.: 10/814,530  
Filing Date: March 31, 2004  
Inventor: Russell Rapport, et al.  
Assignee: Staktek Group L.P.  
Examiner: Thanh Y. Tran  
Art Unit: 2822

**FIRST SUPPLEMENTAL  
INFORMATION DISCLOSURE STATEMENT**

Mail Stop - AMENDMENT  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Pursuant to the guidelines for Information Disclosure Statements, 37 C.F.R. §§ 1.97-1.98, attached hereto are Forms PTO 1449 (2 sheets) with references listed for consideration by the Office.

Pursuant to Official Gazette Notice No. 14, dated August 5, 2003, copies of the listed written U.S. references are not enclosed. However, in accordance with 37 C.F.R. § 1.98(a)(2), copies of the listed written foreign references and articles are being provided.

In accordance with 37 C.F.R. §§ 1.97(g)-(h), the filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b) or prior art under 35 U.S.C. §§ 102 or 103.

The examiner should also be aware of the co-pending applications in the same application family as the above-referenced application. The co-pending applications are: App. No.: 10/400,309 filed 3/27/03; App. No.: 10/136,890 filed 5/02/02; App. No.: 10/435,192 filed on 5/09/03; App. No.: 10/453,398 filed on 6/03/03; App. No.: 10/457,608 filed on 6/09/03; App. No.: 10/631,886 filed on 7/11/03; App. No.: 10/624,097 filed on 7/21/03; App. No.: 10/804,452 filed on 3/19/04; App. No.: 10/814,530 filed on 3/31/04; App. No.: 10/814,532 filed on 3/31/04; App. No.: 10/828,495 filed on 4/20/04; App. No.: 10/836,855 filed on 4/30/04; App. No.: 10/845,029 filed on 5/13/04; App. No.: 10/709,732 filed on 5/25/04; App. No.: 10/872,897 filed on 6/21/04; App. No.: 10/873,847 filed on 6/22/04; App. No.: 10/913,220 filed on 8/06/04; App. No.: 10/914,483 filed on 8/09/04; App. No.: 10/917,216 filed on 8/12/04; App. No.: 10/931,828 filed on 9/01/04; App. No.: 10/958,584 filed on 10/05/04; App. No.: 10/958,924 filed on 10/05/04; App. No.: 10/958,934 filed on 10/05/04; App. No.: 10/963,867 filed on 10/12/04; App. No.: 10/978,149 filed on 10/29/04; App. No.: 11/011,469 filed on 12/14/04.

The Commissioner is hereby authorized to charge any fees which may be required in connection with the filing, or to credit any overpayment to Deposit Account No. 50-0897, upon which account the undersigned has the authority to sign. **A duplicate copy of Forms PTO 1449 are enclosed.**

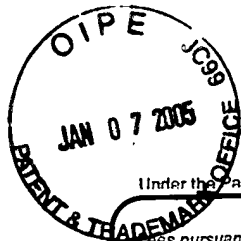
Respectfully submitted,

ANDREWS KURTH LLP

By: \_\_\_\_\_

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1/7/05  
\_\_\_\_\_  
Date



PTO/SB/17 (12-04)

Approved for use through 07/31/2006. OMB 0651-0032  
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Effective on 12/08/2004.

Fees pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4818).

**FEE TRANSMITTAL**  
**For FY 2005**☐ Applicant claims small entity status. See 37 CFR 1.27**TOTAL AMOUNT OF PAYMENT** (\$)**180.00****Complete if Known**

Application Number	10/814,530
Filing Date	March 31, 2004
First Named Inventor	Russell Rapport, et al.
Examiner Name	Thanh Y. Tran
Art Unit	2822
Attorney Docket No.	254-094-CIP4/C1

**METHOD OF PAYMENT (check all that apply)**☐ Check ☐ Credit Card ☐ Money Order ☐ None ☐ Other (please identify): \_\_\_\_\_☒ Deposit Account Deposit Account Number: 50-0897 Deposit Account Name: Andrews Kurth LLP

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

☒ Charge fee(s) indicated below☐ Charge fee(s) indicated below, except for the filing fee☒ Charge any additional fee(s) or underpayments of fee(s) under 37 CFR 1.16 and 1.17☒ Credit any overpayments**WARNING:** Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.**FEE CALCULATION****1. BASIC FILING, SEARCH, AND EXAMINATION FEES**

Application Type	FILING FEES		SEARCH FEES		EXAMINATION FEES		Fees Paid (\$)
	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	
Utility	300	150	500	250	200	100	
Design	200	100	100	50	130	65	
Plant	200	100	300	150	160	80	
Reissue	300	150	500	250	600	300	
Provisional	200	100	0	0	0	0	

**2. EXCESS CLAIM FEES**

Fee Description	Fee (\$)	Small Entity Fee (\$)
Each claim over 20 or, for Reissues, each claim over 20 and more than in the original patent	50	25
Each independent claim over 3 or, for Reissues, each independent claim more than in the original patent	200	100
Multiple dependent claims	360	180

<b>Total Claims</b>	<b>Extra Claims</b>	<b>Fee (\$)</b>	<b>Fee Paid (\$)</b>	<b>Multiple Dependent Claims</b>	<b>Fee (\$)</b>	<b>Fee Paid (\$)</b>
- 20 or HP = _____	x _____	= _____				
HP = highest number of total claims paid for, if greater than 20						
<b>Indep. Claims</b>	<b>Extra Claims</b>	<b>Fee (\$)</b>	<b>Fee Paid (\$)</b>			
- 3 or HP = _____	x _____	= _____				
HP = highest number of independent claims paid for, if greater than 3						

**3. APPLICATION SIZE FEE**

If the specification and drawings exceed 100 sheets of paper, the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).

<b>Total Sheets</b>	<b>Extra Sheets</b>	<b>Number of each additional 50 or fraction thereof</b>	<b>Fee (\$)</b>	<b>Fee Paid (\$)</b>
- 100 = _____	/ 50 = _____	(round up to a whole number) x _____	= _____	

**4. OTHER FEE(S)**

Non-English Specification, \$130 fee (no small entity discount)

Other: Information Disclosure Statement

**Fees Paid (\$)**180**SUBMITTED BY**

Signature		Registration No. 48,990 (Attorney/Agent)	Telephone 512-320-9291
Name (Print/Type)	Nathan H. Calvert		Date 1/07/05

This collection of information is required by 37 CFR 1.136. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

**ANDREWS**  
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January 7, 2005

**VIA EXPRESS MAIL NO. EV4342160024US**

MAIL STOP - AMENDMENT  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

RE:    Application No.:    10/814,530  
       Filing Date:        March 31, 2004  
       Title:                Memory Expansion and Chip Scale Stacking System  
                                 and Method  
       Docket No.:        254-094-CIP4/C1

Dear Commissioner:

Please find enclosed for filing the following:

1.     Fee Transmittal For FY 2005 (1 page) (with duplicate copy);
2.     First Supplemental Information Disclosure Statement (2 pages);
3.     Forms PTO 1449 (2 pages) (with duplicate copy);
4.     10 references; and
5.     a return postcard for confirmation of receipt.

The Commissioner is hereby authorized to charge any fees which may be required to Deposit Account 50-0897. Please return the postcard confirming your receipt of the enclosed materials.

Very truly yours,

Nathan H. Calvert

NHC/at  
Enclosures

Form PTO-1449 (Modified)  
(Rev. 11/92)U.S. Dept. of Commerce  
Patent and Trademark Office

ATTY. DOCKET NO.

SERIAL NO.

254-094-CIP4-C1

10/814,530

**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(Use several sheets if necessary)

APPLICANT

Russell Rapport, et al.

FILING DATE

March 31, 2004

GROUP

2822

**U.S. PATENT DOCUMENTS**

*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)
	US 5,117,282	05/26/1992	Salatino	357	81	10/29/1990
	US 5,448,511	09/05/1995	Paurus, et al.	365	52	06/01/1994
	US 5,642,055	06/24/1997	Difrancesco	324	757	05/12/1995
	US 5,917,709	06/29/1999	Johnson, et al.	361	803	06/12/1997
	US 6,121,676	09/19/2000	Solberg	257	686	12/11/1997
	US 6,205,654 B1	03/27/2001	Burns	29	830	12/28/1998
	US 6,222,737 B1	04/24/2001	Ross	361	767	04/23/1999

**FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION**

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	2003309246(A)	10/31/2003	Japan			Partial	
	2003086761(A)	3/20/2003	Japan			Partial	
	2003086760(A)	3/20/2003	Japan			Partial	
	2003037246(A)	2/7/2003	Japan			Partial	
	2001332683(A)	11/30/2001	Japan			Partial	
	2001085592(A)	3/30/2001	Japan			Partial	
	2001077294(A)	3/23/2001	Japan			Partial	

**OTHER DOCUMENTS**

(INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

	Chip Scale Review Online - An Independent Journal Dedicated to the Advancement of Chip-Scale Electronics. (Website 9 pages) Fjelstad, Joseph, Pacific Consultants L.L.C., Published Jan 2001 on Internet by U.S. Author
	Flexible Thinking: Examining the Flexible Circuit Tapes. (Website 2 pages) Fjelstad, Joseph., Published 4/20/2000 on Internet by U.S. Author
	Die Products: Ideal IC Packaging for Demanding Applications - Advanced packaging that's no bigger than the die itself brings together high performance and high reliability with small size and low cost. (Website 3 pages with 2 figures) Larry Gilg and Chris Windsor. December 23, 2002. Published on Internet

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449 (Modified)  
(Rev. 11/92)

U.S. Dept. of Commerce  
Patent and Trademark Office

**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(Use several sheets if necessary)

(37 CFR 1.98(b))

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10/814,530

APPLICANT

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FILING DATE

March 31, 2004

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**U.S. PATENT DOCUMENTS**

*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)
	US 6,360,433 B1	03/26/2003	Ross	29	840	09/19/2000
	US 6,376,769 B1	04/23/2002	Chung	174	522	03/14/2000
	US 6,552,910 B1	04/22/2003	Moon, et al.	361	749	06/28/2000
	US 6,627,984 B2	09/30/2003	Bruce, et al.	257	686	07/24/2001
	US 6,660,561 B2	12/09/2003	Forthun	438	109	07/24/2002
	US 6,677,670 B2	01/13/2004	Kondo	257	686	04/25/2001
	2004/0150107 A1	08/05/2004	Cha, et al.	257	738	12/29/2003
	2004/0021211 A1	02/05/2004	Damberg	257	282	09/06/2002
	2003/0168725 A1	09/11/2003	Warner et al.	257	686	10/28/2002
	2001/0040793 A1	10/15/2001	Inaba	361	749	01/31/2001

**FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION**

Document Number	Date	Country	Class	Subclass	Translation	
					Yes	No

**OTHER DOCUMENTS**

(INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)


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